

Docket No.: 246442US2S

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 10/730,098

Applicants: Kazuya NISHIHORI, et al.

Filing Date: December 9, 2003

For: HETEROJUNCTION TYPE COMPOUND

SEMICONDUCTOR FIELD EFFECT TRANSISTOR

AND ITS MANUFACTURING METHOD

Group Art Unit: 2814
Examiner: WILLE, D.

SIR:

Attached hereto for filing are the following papers:

ELECTION

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

: EXAMINER: WILLE, D.

SERIAL NO: 10/730,098

KAZUYA NISHIHORI ET AL

FILED: DECEMBER 9, 2003

: GROUP ART UNIT: 2814

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SIR:

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In response to the election requirement dated February 16, 2005, Applicants elect Group I, Claims 1-12, drawn to a device, classified in class 257, subclass 194 for further examination on the merits. Applicants reserve the right to file one or more divisional applications directed to the non-elected claims.

Accordingly, examination on the merits of Claims 1-12 is believed to be in order and an early and favorable action is respectfully requested.

Respectfully submitted,

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